

Amendments to the Specification:

Please replace paragraph [11] with the following amended paragraph:

[11] FIG. 1 is a simplified diagram of a display 100 according to an embodiment of the present invention. The display may include a pair of planar substrates 110, 120 that provide mechanical support to the display 100. The substrates 110, 120 generally are separated from one another and joined by various seals or spacers (not shown). Thus, mechanical elements within the display 100 form [[a]] chambers 130 across the display 100 into which liquid crystal materials are provided. Thin film transistors (TFTs) 140 controls the liquid crystal material at various spatial position across the chamber 130, creating pixels that are selectively opaque or transmissive to light. Thus, controlling the various TFTs 140 causes the display 100 to display image information. TFTs 140 may be provided with associated capacitors (not shown) that maintain the orientation of the LCD material when the TFTs 140 are not actively driven. In this regard, the structure and operation of an LCD display is well known.

Please replace paragraph [14] with the following amended paragraph:

[14] The display 100 may include control lines 190, 200 to provide electrical connectivity between the polymer memory cells PM1, PM2, PM3 and devices external to the display 100 (not shown). As indicated, the conductivity of a polymer memory cell may be controlled to represent digital data. Thus, when a predetermined potential is applied to a 'supply line' on a first portion of the polymer memory cell, the presence or absence of a current on a 'return line' on a second portion of the cell may indicate a state of stored data. In an embodiment having a predetermined number N of the cells in a chamber, there may be N supply conductors 190 (not shown individually) and a single return conductor ~~200~~ or 200 or there may be a single supply conductor 190 and a N return conductors 200 (again, not shown individually) to permit individual addressing of the polymer memory cells within the chamber 150. Other embodiments permit the supply and return conductors 190, 200 to be aligned with but insulated from addressing conductors 210, 220 that drive the transistor 140 and LCD materials, to minimize the profile of all the conductors 190, 200, 210 and 220 with reference to light propagating through the display 100.